

REMARKS

Applicants respectfully requests favorable reconsideration of this application, as amended.

Without acceding to the outstanding rejections under 35 U.S.C. § 103(a), independent Claims 1 and 6 have been amended to recite certain distinctive features of Applicants' invention with greater particularity. Specifically, the claims now recite that the microcomputer chip of Applicants' invention is stacked over the memory chip (or memory chips) such that it covers a portion of the longer side of the memory chip but covers no portion of the shorter side of the memory chip adjacent to the longer side. See, e.g., Fig. 3 of Applicants' drawings.

It is apparent that neither of the references cited in the outstanding rejections, namely Hiroaki et al. (JP2001-291821) and Kanemoto et al. (US Patent 6410987), teaches or suggests an arrangement of microcomputer and memory chips as now particularly set forth in Claims 1 and 6 as discussed above. Indeed, the cited references evidently fail to teach or suggest a microcomputer chip arranged to cover a portion of any side of a memory chip, let alone Applicants' arrangement as now particularly claimed.

Accordingly, Claims 1 and 6 are clearly patentable over the applied references and are now in condition for

allowance. The remaining claims, being dependent from Claims 1 and 6, are patentable for at least the reasons discussed above.

It is therefore respectfully urged that the outstanding rejections be withdrawn and that this application now be passed to issue.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 (XA-10006) any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been separately requested, such extension is hereby requested.

Respectfully submitted,

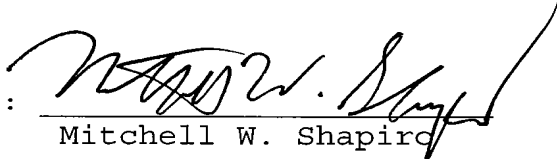
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